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**\*\* CONTINUING DATA \*\*\*\*\***THIS APPLICATION IS A 371 OF PCT/FR99/02724 11/08/1999 *JP***\*\* FOREIGN APPLICATIONS \*\*\*\*\***FRANCE 98 14071 11/09/1998 *JP***IF REQUIRED, FOREIGN FILING LICENSE GRANTED**

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35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met <i>allowance</i>				
Verified and Acknowledged <i>J M E</i> Examiner's Signature	<i>JD</i> Initials			

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**TITLE**

Parallel architecture digital filter and spread spectrum signal receiver using such a filter

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